IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Minnick, Linden	Art Unit	: 2194
Serial No.	: 10/003,134	Examiner	: TRUONG, Lechi
Filed	: 11/15/2001	Assignee	: Intel Corporation
Title	: METHOD FOR INDICATING COMPLETION STATUS OF ASYNCHRONOUS EVENTS		

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Commissioner for Patents
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PRE-APPEAL BRIEF REQUEST FOR REVIEW

Claim 23 recites a controller that includes multiple encryption units including an Advanced Encryption Standard (AES) encryption engine and a Data Encryption Standard (DES) engine. Claim 23 stands rejected as being unpatentable under 35 U.S.C. 103(a) over Harrington (U.S. 4,939,644) in view of Bonevento (U.S. 5,131,082) and further in view of Pang (U.S. 6,366,117). In particular, the rejection states that Pang teaches commands executed by one of multiple encryption units implementing different respective encryption algorithms where the different encryption units comprise an AES engine and a DES engine. While Pang does list different encryption algorithms known in the prior art (col. 1, lines 31-44), Pang does not teach both an AES engine and a DES engine co-existing in the FPGA of Pang. In other words, Pang's

acknowledgement that DES and AES exist does not constitute a teaching of both a DES encryption unit and an AES encryption unit in Pang. Hence, the attribution of such teaching to Pang constitutes clear error and, in turn, the 103 rejection constitutes clear error.

Applicant, thus, respectfully requests withdrawal of the rejection of claim 23 and its corresponding dependent claims. Applicant similarly requests withdrawal of the rejection of independent claim 33 and its corresponding dependent claims.

Respectfully submitted,

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